

**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2116**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/756,551	§	Examiner:	Patel, Nitin C.
Filed:	January 13, 2004	§	Group/Art Unit:	2116
Inventor(s):		§	Atty. Dkt. No:	5500-95300
Michael T. Wisor		§		
		§		
		§		
		§		
Title:	Hardware Initialization	§		
	Method that is Independent	§		
	of Boot Code Architecture	§		
		§		
		§		
		§		
		§		

**RESPONSE TO OFFICE ACTION OF
December 20, 2006**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of December 20, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.